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Substitute for form 1449B/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		<i>Application Number</i>	10/683,929
		<i>Filing Date</i>	October 9, 2003
		<i>First Named Inventor</i>	John W. Rapp
		<i>Art Unit</i>	2818
		<i>Examiner Name</i>	
(Use as many sheets as necessary)		<i>Attorney Docket Number</i>	1934-13-3
Sheet	2	of	2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/DJH/		International Search Report for PCT/US2005/035818 dated August 4, 2006	

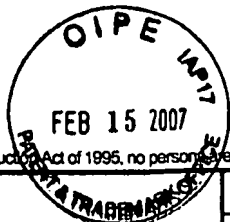
Examiner Signature	/David Huisman/	Date Considered	05/07/2007
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**INFORMATION DISCLOSURE  
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Sheet

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/DJH/		BAKSHI S; GAJSKI D D; "Partitioning and Pipelining for Performance-Constrained Hardware/Software Systems", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 7, NR. 4, PG. 419-432, (1999-12-00), XP000869229	

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Signature

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***(Use as many sheets as necessary)***

Sheet	1	of	2
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
/DJH/		EP 1 061 438 A1	12/20/00	Hewlett-Packard Company		
/DJH/		EP 0 945 788 A2	09/29/99	Texas Instruments Incorporated		
/DJH/		EP 0 945 788 A3	06/26/02	Texas Instruments Incorporated		
/DJH/		EP 0 694 847 A2	01/31/96	International Business Machines Corporation		

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/DJH/		LECURIEUX-LAFAYETTE G: "Un Seul FPGA Dope Le Traitement D'Images", Electronique, CEP Communication, Paris, FR, no. 55, 1996, pp. 98, 101-103	
/DJH/		VERMEULEN F. ET AL., "Flexible Hardware Acceleration for Multimedia Oriented Microprocessors", Micro-33. Proceedings of the 33 <sup>rd</sup> Annual ACM/IEEE International Symposium on Microarchitecture, Monterey, CA, Dec. 10-13, 2000, Proceedings of the Annual ACM/IEEE International Symposium on Microarchitecture, Los Alamitos, CA: IEEE Comp. Soc. US, 10 December 2000, pp. 171-177	
/DJH/		International Search Report for PCT/US 03/34557, December 21, 2004	
/DJH/		International Search Report for PCT/US 03/34559, January 7, 2005	

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